GALGOTIAS UNIVERSITY

DEECE

PROGRAM STRUCTURE _2023-24

Curriculum Structure of B.Tech in Electronics Engineering (VLSI Design and Technology)

Sl. No	Name of the Course	Course Code	L	T	P	S	C	ETE	COURSE TYPE
Semester 1									
1	Multi Variable Calculus	C1UC121B	3	0	1		4	100	INT
2	Programming for Problem Solving – C	E2UC101C	3	0	1	1	5	100	COMP
3	Basic Electrical & Electronics Engineering	G2UA120B	3	0	1	0	4	100	INT
4	Semi-conductor Physics	C1UD121B	3	0	1		4	100	INT
5	Engineering Graphics & Introduction to Digital Fabrication	G3UB120B	3	0	1		4	100	INT
6	Communication Skills for Engineers	K1UC120B	2	0	1		3	100	INT
Semester II									
1	Biology for Engineers*	C2UD121B	3	0	1		4	100	INT
2	Linear Algebra & Differential Equations	C1UC220T	3	0	0		3	100	THEORY
3	Introduction to Digital Systems	G2UC101B	2	0	1	0	3	100	INT
4	OOPS	E2UC201C	3	0	1	1	5	100	COMP
5	Discrete Mathematics	E1UJ204T	3	0	0		3	100	THEORY
Semester III									
1	Functions of Complex Variables and Transforms	**********	3	0	0		3	100	THEORY
2	Electronic Devices and Circuits	**********	3	0	1	1	5	100	COMP
3	Network Analysis and Synthesis	**********	3	0	0		3	100	THEORY
4	Signals and Systems	**********	3	0	0		3	100	THEORY
5	Electromagnetic Field Theory	**********	3	0	0		3	100	THEORY
6	English Competency and Aptitude Building - 1	**********	0	0	2		2	100	LAB
Semester IV									
1	Probability and Stochastic Process	C1UC420T	3	0	0		3	100	THEORY
2	Analog and Digital Electronics		3	0	1		4	100	INT
3	Analog and Digital Communication	G2UA402T	3	0	1		4	100	INT
4	Microproccesor and Microcontrollers		3	0	1	1	5	100	COMP
5	Antenna and Wave Propagation	**********	3	0	0		3	100	THEORY

6	English Competency and Aptitude Building - 2	K1UC420L	0	0	2		2	100	LAB
Semester V									
1	VLSI Design	**********	3	0	1	1	5	100	COMP
2	Digital Signal Processing	**********	3	0	1		4	100	INT
3	Control Systems	**********	3	0	0		3	100	THEORY
4	Embedded Systems	**********	3	0	0		3	100	THEORY
6	Enterpreneurship	**********	3	0	0		3	100	THEORY
7	English Competency and Aptitude Building - 3	**********	0	0	2		2	100	LAB
Semester VI									
1	Campus to Corporate Training	**********	0	0	2		2	100	LAB
2	Semiconductor Equipment Design and Technology	**********	3	0	1		4	100	INT
3	VLSI Verification and Testing	**********	3	0	1	1	5	100	COMP
5	Program Elective -I	**********	3	0	0		3	100	THEORY
6	Program Elective-II	**********	3	0	0		3	100	THEORY
7	Semiconductor Materials Synthesis and Characterization	**********	3	0	0		3	100	THEORY
Semester VII									
1	Program Elective-III	********	3	0	0		3	100	THEORY
2	Program Elective-IV	********	3	0	0		3	100	THEORY
3	Open Elective -1	********	3	0	0		3	100	THEORY
4	Open Elective -2	********	3	0	0		3	100	THEORY
5	Capstone Design - I	********	0	0	8		8	100	Project
Semester VIII									
1	Capstone Design - II	********	0	0	20		20	50	Project